AN OVERLAP SAVE ALGORITHM FOR BLOCK CONVOLUTION WITH REDUCED COMPLEXITY

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ABSTRACT

We propose a block convolution algorithm that requires shorter length FFT than the conventional overlap save algorithm (OSA). It is shown that the OSA can be split into two separate processes related to the previous and current data blocks. Hence, only current data block needs to be transformed in the proposed OSA, whereas the concatenated block of previous and current data is transformed in the conventional method. As a result, the number of arithmetic operations for the block convolution is reduced. Also, the reduced transform size gives additional advantage in data manipulation when implemented on DSP and PC.

Index Terms— Block Convolution, Overlap Save Algorithm, Fast Fourier Transform (FFT)

1. INTRODUCTION

It is well known that the block convolution is often implemented by OSA using the FFT [1]. There is another approach using the number theoretic transform, which has advantages when implemented in fixed point arithmetic [2]. There are also variants of block convolution methods considering many aspects of implementation such as input-output delay and hardware/software realizations [3, 4]. For the software implementation of block convolution on modern computers with floating point arithmetics, the conventional OSA is still the most popular method because of its simple data manipulation and the availability of powerful FFT libraries. For example, audio processing that requires large length convolutions employs the conventional OSA as a basic building block [5, 6], and the block adaptive filtering is also implemented with the conventional OSA [7]. Hence, we focus on the reduction of computational complexity in the conventional OSA, and propose a new algorithm that requires the half-sized transform. The proposed algorithm separates the computations required for the previous and current block, whereas the conventional OSA computes the transform of the overall block at once. For the efficient implementation of this scheme, a new transform is required, which will be called quarter-DFT (QDFT). The QDFT can be implemented by twiddle factor multiplications to the input followed by a standard DFT. Comparison of computational complexity shows that the OSA based on the proposed QDFT reduces the number of arithmetic operations in proportion to the transform length. In addition to reduced computational complexity, the reduction of transform size gives additional advantage of reduced memory access time, which is demonstrated by comparing the computation times taken by the proposed and conventional methods on PC and DSP.

The rest of this paper is organized as follows. We summarize the notation and review the conventional OSA in Section 2. The proposed algorithm is presented in Section 3, and computational complexity analysis and the results of implementation on DSP and PC are shown in Section 4. Finally, conclusions are given in Section 5 and issue for and efficient implementation of QDFT is discussed in Section 6.

2. REVIEW OF OSA

We first summarize notations and definitions used in the rest of paper, and then review the conventional OSA.

2.1. Summary of Notations and Definitions

\[ L : \text{Filter length} \]
\[ M : \text{Number of input data at each step in OSA, } M \geq L \]
\[ N = 2M, \text{the size of overall block in OSA} \]
\[ s_n : \text{Input sequence} \]
\[ h_n : \text{Filter coefficients } (n = 0, 1, \cdots, L - 1) \]
\[ r_n : \text{Output sequence (Result of linear convolution } s_n * h_n \text{)} \]
\[ W_N = e^{-j \frac{2\pi}{N}} \]
\[ x_p : \text{Previous data vector of size } M \text{ in OSA} \]
\[ x_c : \text{Current data vector of size } M \text{ in OSA} \]
\[ x = [x_0, x_1, \cdots, x_{N-1}]^T = [x_p^T : x_c^T]^T \]
\[ f = [h_0, h_1, \cdots, h_{L-1}, 0, \cdots, 0]^T \]
\[ (\text{Size } = M. \text{ Zeros are padded when } M > L) \]
\[ 0_M : \text{zero vector of size } M \]
\[ g = [g_0, g_1, \cdots, g_{N-1}]^T = [f^T : 0_M^T]^T \]
\[ y = [y_0, y_1, \cdots, y_{N-1}]^T : \text{temporary output vector} \]
\( D_K : \text{DFT matrix of size } K \)
\((n,k)\)-th element is \( W_{nk}^K \)
\( \Theta_K : \text{odd-DFT (ODFT) matrix of size } K \)
\((n,k)\)-th element is \( W_{nk}^{K+1/2} \)
\( Q_K : \text{quarter-DFT (QDFT) matrix of size } K \)
\((n,k)\)-th element is \( W_{nk}^{K+3/4} \)

\[ D_K : DFT \text{ matrix of size } K \]
\((n,k)\)-th element is \( W_{nk}^{K} \)
\( \Theta_K : \text{odd-DFT (ODFT) matrix of size } K \)
\((n,k)\)-th element is \( W_{nk}^{K+1/2} \)
\( Q_K : \text{quarter-DFT (QDFT) matrix of size } K \)
\((n,k)\)-th element is \( W_{nk}^{K+3/4} \)

\[ X^d = (D_N)x, \quad X^o = (\Theta_N)x, \quad X^q = (Q_N)x \]
\[ G^d = (D_N)g, \quad G^o = (\Theta_N)g, \quad G^q = (Q_N)g \]
\[ F^d = (D_M)f, \quad F^o = (\Theta_M)f, \quad F^q = (Q_M)f \]
\[ X_p^d = (D_M)x_p, \quad X_p^o = (\Theta_M)x_p, \quad X_p^q = (Q_M)x_p \]
\[ X_c^d = (D_M)x_c, \quad X_c^o = (\Theta_M)x_c, \quad X_c^q = (Q_M)x_c \]

\[ y = [y_0, y_1, \cdots, y_{N-1}]^T : \text{temporary output vector} \]
\( (\text{circular convolution of } g \text{ and } x) \)

2.2. Conventional OSA

Using above notations, the conventional OSA can be summarized as follows:

Prepare \( G^d \). Copy \( x_c \) into \( x_p \), and get a block of new \( M \) data \( \{s_n, s_{n+1}, \cdots, s_{n+M-1}\} \) into \( x_p \). Compute \( X^d \), and let \((X^d \otimes G^d)\) be \( Y \), where \( \otimes \) implies element-wise multiplication. Perform IDFT on \( Y \) to get \( y \), discard the first half of data and save the last half. The saved data is the desired output \( \{r_n, \cdots, r_{n+M-1}\} \). Above process is repeated for the next data block (except for the preparation of \( G^d \)).

3. PROPOSED ALGORITHM

The main idea of proposed algorithm is to separate the computations for previous and current block in OSA. Then, we exploit the computations that were already performed for the previous block, when computing the convolution for the current block. This is based on the observation of decimation-in-frequency FFT algorithm, where the DFT \( X^d \) is split into shorter transforms as

\[ \begin{bmatrix} X_e \\ X_o \end{bmatrix} = \begin{bmatrix} D_M & D_M \\ \Theta_M & -\Theta_M \end{bmatrix} \begin{bmatrix} x_p \\ x_c \end{bmatrix}. \]  

(1)

From this, the OSA can also be split into the computation of even and odd indexed terms. That is, splitting \( G^d \) into \( G_e \) and \( G_o \), spectral multiplication with \( X_e \) and \( X_o \), followed by inverse transform gives the circular convolution result, which is denoted as

\[ y = \begin{bmatrix} D_M^{-1} & \Theta_M^{-1} \\ D_M^{-1} & -\Theta_M^{-1} \end{bmatrix} \begin{bmatrix} G_e \otimes X_e \\ G_o \otimes X_o \end{bmatrix}. \]  

(2)

But, since first half of \( g \) is \( f \) and the rest is \( 0 \), we have

\[ \begin{bmatrix} G_e \\ G_o \end{bmatrix} = \begin{bmatrix} D_M & D_M \\ \Theta_M & -\Theta_M \end{bmatrix} \begin{bmatrix} f \\ 0_M \end{bmatrix} = \begin{bmatrix} F^d \\ F^o \end{bmatrix}. \]  

(3)

Also from (1) and the definitions, it can be seen that \( X_e = X_p^d + X_c^d \) and \( X_o = X_p^o - X_c^o \). Hence, the circular convolution in (2) can be rewritten as

\[ y = \begin{bmatrix} D_M^{-1} & \Theta_M^{-1} \\ D_M^{-1} & -\Theta_M^{-1} \end{bmatrix} \begin{bmatrix} F^d \otimes (X_p^d + X_c^d) \\ F^o \otimes (X_p^o - X_c^o) \end{bmatrix}. \]  

(4)

which can be separated into the computations of previous and current blocks as

\[ y = \begin{bmatrix} D_M^{-1} (F^d \otimes X_p^d) + \Theta_M^{-1} (F^o \otimes X_p^o) \\ D_M^{-1} (F^d \otimes X_p^o - \Theta_M^{-1} (F^o \otimes X_p^o) \end{bmatrix} + \begin{bmatrix} D_M^{-1} (F^d \otimes X_c^d) - \Theta_M^{-1} (F^o \otimes X_c^o) \\ D_M^{-1} (F^d \otimes X_c^o) + \Theta_M^{-1} (F^o \otimes X_c^o) \end{bmatrix}. \]  

(5)

In the computation for the current block (the second term in the above equation), it seems that two kinds of convolution are needed, namely \( D_M^{-1} (F^d \otimes X_p^d) \) and \( \Theta_M^{-1} (F^o \otimes X_p^o) \). For the convenience in notations and analysis, let us consider \( D_M^{-1} (G^d \otimes X_p^d) \) and \( \Theta_M^{-1} (G^o \otimes X_p^o) \) instead of the above \( M \)-point transforms, without loss of generality. In the time domain, \( D_M^{-1} (G^d \otimes X_p^d) \) corresponds to the standard circular convolution as

\[ y_n = \sum_{k=0}^n x_k g_{n-k} + \sum_{k=n+1}^{N-1} x_k g_{n-k+N}. \]  

(6)

and \( \Theta_M^{-1} (G^o \otimes X_p^o) \) corresponds to the skew circular convolution as

\[ y_n = \sum_{k=0}^n x_k g_{n-k} - \sum_{k=n+1}^{N-1} x_k g_{n-k+N}. \]  

(7)

Hence, the addition of the two terms \( D_M^{-1} (G^d \otimes X_p^d) + \Theta_M^{-1} (G^o \otimes X_p^o) \) corresponds to time domain computation

\[ y_n = \sum_{k=0}^n x_k g_{n-k}, \]  

(8)

and the subtraction \( D_M^{-1} (G^d \otimes X_p^d) - \Theta_M^{-1} (G^o \otimes X_p^o) \) to

\[ y_n = \sum_{k=n+1}^{N-1} x_k g_{n-k+N}. \]  

(9)

It is found that these two terms can actually be obtained from a single transform domain processing “QDFT.” As defined in section 2, the input-output relationship of \( N \)-point QDFT is

\[ X_k^q = \sum_{n=0}^{N-1} x_n W_N^q (k+\frac{1}{2}). \]  

(10)

It can be easily shown that the spectral multiplication and inverse transform in the QDFT domain, i.e., \( \Theta_M^{-1}(G^o \otimes X_p^o) \) corresponds to the time domain computation given by
Comparing (8) and (9) with (11), we can see that the real part of $Q_N^{-1}(G^q \otimes X^q)$ is equivalent to $D_N^{-1}(G^d \otimes X^d) + \Theta_N^{-1}(G^q \otimes X^q)$ and the imaginary part to $D_N^{-1}(G^d \otimes X^d) - \Theta_N^{-1}(G^q \otimes X^q)$. In the same manner, it can be shown that $D_M^{-1}(F^d \otimes X^q) + \Theta_M^{-1}(F^q \otimes X^q)$ in (5) can be replaced by the real part of $Q_M^{-1}(F^q \otimes X^q)$ and $D_M^{-1}(F^d \otimes X^d) - \Theta_M^{-1}(F^q \otimes X^q)$ by the imaginary part. In summary, (5) is equivalent to

$$y = \left[ \begin{array}{c} \Re\{Q^{-1}_M\{F^q \otimes X^q\}\} \\ \Im\{Q^{-1}_M\{F^q \otimes X^q\}\} \\ \Im\{Q^{-1}_M\{F^q \otimes X^q\}\} \\ \Re\{Q^{-1}_M\{F^q \otimes X^q\}\} \end{array} \right].$$

Hence, we need to compute only the second term (QDFT, spectral multiplication, and IQDFT of current data block $x_n$) at each step. For describing the proposed algorithm with the above notations, let us define new vectors of size $M$ : $v$ that stores $\Im\{Q^{-1}_M\{F^q \otimes X^q\}\}$, and $z_r$ and $z_i$ that store the real and imaginary part of $Q_M^{-1}(F^q \otimes X^q)$ respectively. Then the proposed algorithm can be summarized as follows:

1. Prepare $F^q$. Store $z_r$ into $v$. Get a new block of input data into $x_n$. Compute $X^q$. Compute $Q^{-1}_M\{F^q \otimes X^q\}$ and store the real part of this result into $z_r$, and the imaginary part to $z_i$. Then $v + z_r$ is the linear convolution result for the current input data. Repeat above process for the next input block.

### 4. COMPLEXITY ANALYSIS

All the transforms in this paper are implemented based on the radix-4 FFT which is widely used in DSP and PC library[8, 9]. And $M$ is assumed to be power of 4 for the convenience of complexity analysis. The computational complexity of radix-4 FFT can be summarized as

$$\mu_M^c = \frac{3}{2} M \log_2 M$$

$$\mu_M^r = M \log_2 M$$

$$\mu_M = \frac{3}{2} M \log_2 M + 2M$$

$$\alpha_M = M \log_2 M + M$$

where $\mu_n$ and $\alpha_n$ mean the number of multiplications and additions for the $n$-point transform respectively, and the superscript $c$ and $r$ mean the complex and real data respectively. It is noted that $2M$ point real FFT can be calculated efficiently from the computation of one $M$ point complex FFT with additional $2M$ additions by simple data manipulation [8]. In the case of conventional OSA, the $2M$-point FFT is performed on the real data. Then complex spectral multiplication is performed, and IDFT on the complex data is performed. Therefore, the total number of multiplications for the conventional OSA is $\mu_{2M} + M + 1 + \mu_{2M}$, and the number of addition is $\alpha_{2M} + \alpha_{2M}$.

### Table 1. Computational complexity of block convolution with DFT and QDFT

<table>
<thead>
<tr>
<th></th>
<th>MULT</th>
<th>ADD</th>
</tr>
</thead>
<tbody>
<tr>
<td>DFT</td>
<td>$\frac{3}{2} M \log_2 M + 5M + 1$</td>
<td>$2M \log_2 M + 2M$</td>
</tr>
<tr>
<td>QDFT</td>
<td>$\frac{3}{2} M \log_2 M + 3M$</td>
<td>$2M \log_2 M$</td>
</tr>
</tbody>
</table>

### Table 2. Number of arithmetic operations (sum of mults + adds)

<table>
<thead>
<tr>
<th>$M$</th>
<th>DFT</th>
<th>QDFT</th>
<th>savings(%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>512</td>
<td>16257</td>
<td>14208</td>
<td>12.6</td>
</tr>
<tr>
<td>1024</td>
<td>35329</td>
<td>31232</td>
<td>11.6</td>
</tr>
<tr>
<td>2048</td>
<td>76289</td>
<td>68096</td>
<td>10.7</td>
</tr>
<tr>
<td>4096</td>
<td>163841</td>
<td>147456</td>
<td>10.0</td>
</tr>
</tbody>
</table>

From the input-output relationship of QDFT in eq. (10), it can be seen that the QDFT can be implemented by twiddle factor multiplication to the input (multiplication of $W_N^{2n}$ to the $n$-th input), followed by standard DFT. Hence, the computational complexity for the block convolution with the QDFT is: mult = $\mu_M^c + M + \mu_M^c + M$, and add = $\alpha_M + \alpha_M$, where the last $M$ of add is for the real addition with the result of previous block. These results are summarized in Table 1 with respect to the number of input data $M$. Also for several lengths, specific numbers of arithmetic operations are compared in Table 2.

It is evident that the advantage of the proposed algorithm diminishes as the convolution length increases, because both algorithms require $O(M \log_2 M)$ complexity and the difference is just proportional to $M$. However, the long convolution is usually split into smaller ones in order to reduce the output delay which is inevitable in block processing [5, 6]. In other words, the long convolution is usually implemented by many of smaller length convolutions. In these practical implementations, replacing the conventional OSA by the proposed one would greatly reduce the computation times.

The conventional and proposed algorithms are implemented on two platforms - general PC equipped with Intel Pentium 4 processor 3 GHz and Analog Devices BLACKFIN 533 DSP board. For PC implementation of Radix-4 FFT, the state of art Intel IPP (Integrated Performance Primitives) library [9] is exploited. FFT in technical library provided by analog devices is used for DSP simulation. Convolutions are performed for 100 different input data sequences, and the averaged elapsed times are compared. Table 3 and Table 4 show the average elapsed times on PC and DSP respectively.

The savings of actual elapsed time in Table 3 are similar in tendency to those in Table 2 but larger than expected. This shows that the savings come not only from the reduced num-
ber of arithmetic (Table 2) but also from the reduced transform length \(N(=2M)\) vs. \(M\) which results in reduced data access time when manipulating data. On the other hands, Table 4 does not have similar tendency to the expected result (Table 2). Generally DSP does not support sufficient hardware resources such as cache and memory compared with PC, and thus the time taken by memory access in DSP is more influential to the overall performance than in the PC. Hence the savings are getting larger as data size \(M\) increases. However, we can see that there are abrupt increase in savings when \(M=1024\) and abrupt decrease when \(M=2048\). In the case of abrupt increase when \(M=1024\), severe increase of elapsed time in the conventional method occurs due to the limit of cache size and thus it causes the increase in savings. On the other hand there is severe increase in the proposed method when \(M=2048\) because proposed method can perform 2 times larger OSA under the same cache size than the conventional method. Thus abrupt increase of elapsed time in the proposed method results in decrease in savings when \(M=2048\).

### Table 3. Elapsed time of conventional OSA and proposed OSA on PC (\(\mu s\))

<table>
<thead>
<tr>
<th>(M)</th>
<th>conventional</th>
<th>proposed</th>
<th>savings(%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>512</td>
<td>14.69</td>
<td>10.89</td>
<td>25.87</td>
</tr>
<tr>
<td>1024</td>
<td>32.72</td>
<td>24.99</td>
<td>23.62</td>
</tr>
<tr>
<td>2048</td>
<td>67.40</td>
<td>54.30</td>
<td>19.44</td>
</tr>
<tr>
<td>4096</td>
<td>139.24</td>
<td>117.52</td>
<td>15.60</td>
</tr>
</tbody>
</table>

### Table 4. Elapsed time of conventional OSA and proposed OSA on DSP (\(\mu s\))

<table>
<thead>
<tr>
<th>(M)</th>
<th>conventional</th>
<th>proposed</th>
<th>savings(%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>512</td>
<td>43.39</td>
<td>37.49</td>
<td>13.61</td>
</tr>
<tr>
<td>1024</td>
<td>121.49</td>
<td>83.12</td>
<td>31.58</td>
</tr>
<tr>
<td>2048</td>
<td>255.80</td>
<td>209.32</td>
<td>18.17</td>
</tr>
<tr>
<td>4096</td>
<td>613.51</td>
<td>423.84</td>
<td>30.92</td>
</tr>
</tbody>
</table>

### 5. CONCLUSIONS

We have proposed a new OSA that needs half size FFT compared to he conventional OSA. Since the FFT is the main process in the OSA, the proposed algorithm requires less computations and memory access time than the conventional method. The computations for the previous and current blocks in OSA are completely separated, and each block is efficiently computed by QDFT. The real part of QDFT domain convolution for current block plus the imaginary part of previous is shown to be the linear convolution result for the current block.

### 6. ACKNOWLEDGEMENT

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### 7. REFERENCES


